

ETE701T: Digital signal processing processor and Architecture (Theory)

After the completion of Course, Students will be able to.....

		Blooms Level	PO
ETE701T.1	Explain and Demonstrate the detailed architecture, addressing mode, instruction sets of TMS320C5X.	Level 6,2	PO 1
ETE701T.2	Build the program of DSP processor	Level 3	PO 1, 2
ETE701T.3	Design & implement DSP algorithm using code composer studio	Level 6	PO 1, 2
ETE701T.4	Design decimation filter and interpolation filter	Level 6	PO1, 2
ETE701T.5	Illustrate and elaborate the detailed architecture of TMS320C54X	Level 2,6	PO1, 2
ETE701T.6	Illustrate and elaborate the detailed architecture TMS320C6X	Level 2,6	PO 1

Name and Sign of Course Teacher

ETE701P: Digital Signal processing processor and Architecture (Practical)

After the completion of Course, Students will be able to.....

		Blooms Level	PO
ETE701P.1	Illustrate the architecture of TMS and Motorola Processors.	Level 2	PO 1,4
ETE701P.2	Develop the different processing algorithms on DSP processors	Level 6	PO 3,4
ETE701P.3	Design different types of filters and study their characteristics.	Level 6	PO 2,4

Name and Sign of Course Teacher

ETE702T : Television and Video Engineering(Theory)

After the completion of Course,student will be able to...

		Blooms Level	PO
ETE702T.1	Define and Explain basic Elements and parameter of T.V system.	Level 1,2	PO 1
ETE702T.2	Illustrate and Analyze colour T.V. System	Level 2	PO 2
ETE702T.3	Classify and Compare fundamental techniques of Different T.V. standards.	Level 2	PO 1
ETE702T.4	Classify and Interpret Advanced T.V. Technology	Level 2, 5	PO 1,5,6
ETE702T.5	Identify different video recording, display and its consumer application.	Level 3	PO 1,5
ETE702T.6	Elaborate and Analyze basic principles of Digital Video compression techniques.	Level 4,6	PO 1,7

Name and Sign of Course Teacher

ETE702P: Television and Video Engineering (Practical)

After the completion of Course, student will be able to...

		Blooms Level	PO
ETE702P.1	Classify the concept of troubleshoot and repair	Level 2	PO 1,3
ETE702P.2	Develop an understanding of electronics, mechanical and environmental factors involved in maintaining television equipment.	Level 3	PO1, 7
ETE702P.3	Define and Analyze TV Pictures, Composite Video Signal, TV Receiver Picture Tubes.	Level 2,4	PO 1,2

Name and Sign of Course Teacher

ECE703T: Optical Communication (Theory)

After the completion of Course, Students will able to

		Blooms Level	PO
ECE703T.1	Illustrate the basic elements of optical fiber	Level 2	PO 2
ECE703T.2	Examine the different kinds of losses, signal distortion in optical wave guides & other signal degradation factors.	Level 4	PO 1
ECE703T.3	Classify various optical source materials, LED structures, LASER diodes	Level 2	PO 1
ECE703T.4	Examine the fiber optic receivers such as PIN, APD diodes, receiver operation & performance.	Level 4	PO 10
ECE703T.5	Analyze the performance parameters of analog and digital link.	Level 4	PO 10
ECE703T.6	Inspect the operational principal of WDM, SONET, measurement of attenuation, dispersion, refractive index profile in optical fibers.	Level 4	PO 10

Name and Sign of Course Teacher

ETC 704 T: ADVANCED DIGITAL SYSTEM DESIGN (Theory)

After the completion of Course, Students will be able to.....

		Blooms Level	PO
ETE704T.1	explain and understand the basic foundation course in VHDL	Level 5	PO 1
ETE704T.2	address the challenges in Hardware design	Level 6	PO 1, 2
ETE704T.3	discuss the role of digital components in system design	Level 6	PO 1, 4
ETE704T.4	concentrate on HDL based digital design, HDL terminology, Architecture and design of combinational and sequential circuit.	Level 6	PO 2
ETE704T.5	learn about modeling of system tested with test benches .	Level 6	PO 2
ETE704T.6	Illustrate synthesis of digital system and Implementation on FPGA / CPLD	Level 2	PO 1, 4

Name and Sign of Course Teacher

**ETE 704-P: ADVANCED DIGITAL SYSTEM DESIGN
(Practical)**

By the end of the Course, Students Will be able to.....

		Blooms Level	PO
ETE704P.1	Design of Combinational & Sequential Circuits	Level 6	PO 1
ETE704P.2	Develop skilled VLSI front end design	Level 3	PO 2,4
ETE704P.3	Implement and construct the digital system	Level 3	PO 2,4
ETE704P.4	Experiment with Hardware / Software co-design .	Level 3	PO 4

Name and Sign of Course Teacher

ETE705T : VLSI SIGNAL PROCESSING(Theory) :ELECTIVE 1

After the completion of Course, Student will be able to

		Blooms Level	PO
ETE705T.1	Define and explain the advances in DSP application and in scaled VLSI technology.	Level 1,2	PO 1
ETE705T.2	Analyze various methodologies to optimize power delay .	Level 4	PO 1, 2
ETE705T.3	Build Real Time processing system.	Level 3	PO 1,3
ETE705T.4	Design of algorithm structure for DSP algorithms based on algorithm transformation	Level 6	PO 3
ETE705T.5	Illustrate and elaborate different algorithm to reduced the power consumption.	Level 2,6	PO 1,4
ETE705T.6	Analyze various methodologies to optimize area of VLSI design.	Level 4	PO 1,6

Name and Sign of Course Teacher

ETE705 T: Microelectro- Mechanical System and SoC ELECTIVE 2 (Theory)

After the completion of Course, Students will be able to.....

		Blooms Level	PO
ETE705 T.1	Illustrate working principle of current micro sensors , Actuators used in Microsystems	Level 2	PO 1
ETE705 T.2	Apply scaling laws that are used extensively in the conceptual design of micro devices and systems	Level 3	PO 1, 2
ETE705 T.3	Illustrate the basic principles and application of- fabrications processes like photolithography , ion Implantations, diffusion, oxidation , CVD, PVD and etching	Level 2	PO 1, 4
ETE705 T.4	Select a micromachining technique like bulk micromachining and surface micromachining for a MEMS fabrication process.	Level 1,3	PO 2
ETE705 T.5	Develop recent technologies in the field of MEMS and Devices.	Level 2,6	PO 2
ETE705 T.6	Design of System on Chip, Microsystems technology.	Level 6	PO 1, 4

Name and Sign of Course Teacher